

Field	Number of Bits	Description
AM_ADDR	3	active member address
TYPE	4	type code
FLOW	1	flow control
ARQN	1	acknowledge indication
SEQN	1	sequence number
HEC	8	header error check

FIG. 1

User Number	1	2	3	4	5	6	7	8	9	10	11	12	13	14
AM_ADDR	001	010	011	100	101	110	111	111	111	111	111	111	111	111
TYPE	xxx	xxx	xxx	xxx	xxx	xxx	000	001	010	011	100	101	110	111

FIG. 2

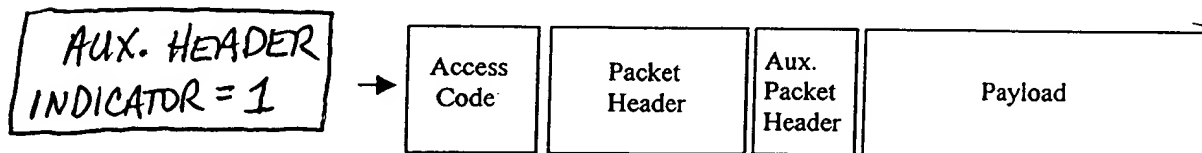


FIG. 3

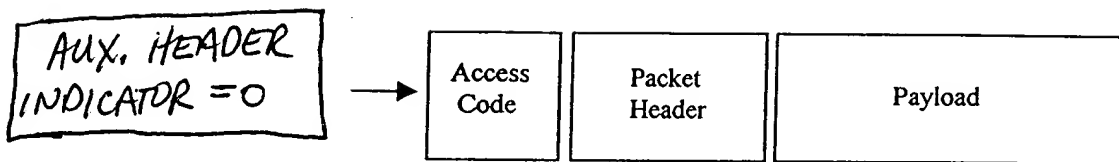


FIG. 4

Field	Number of Bits	Description
AM_ADDR	4	active member address
TYPE	4	type code
FLOW	1	flow control
ARQN	1	acknowledge indication
SEQN	1	sequence number
HEC	8	header error check

FIG. 11

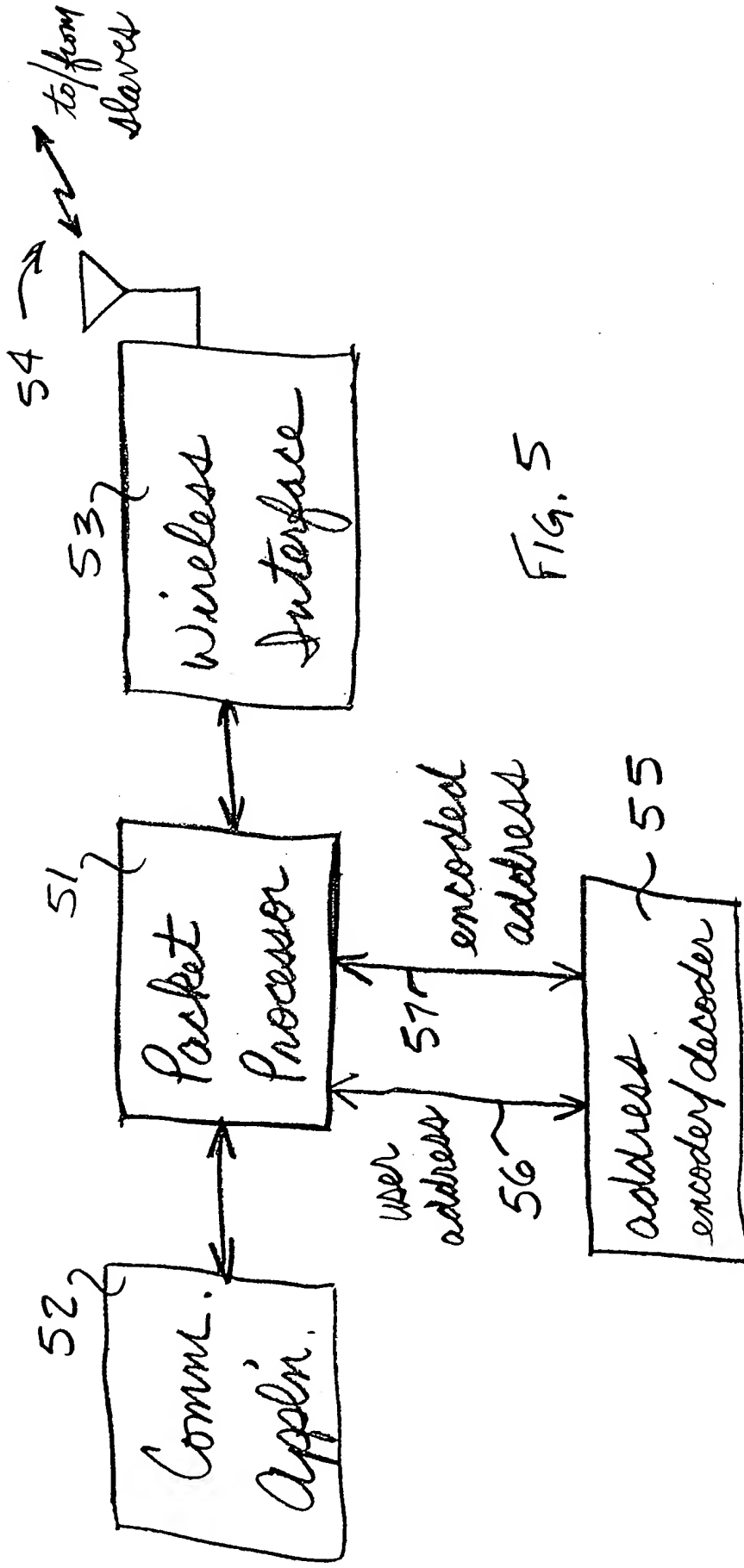
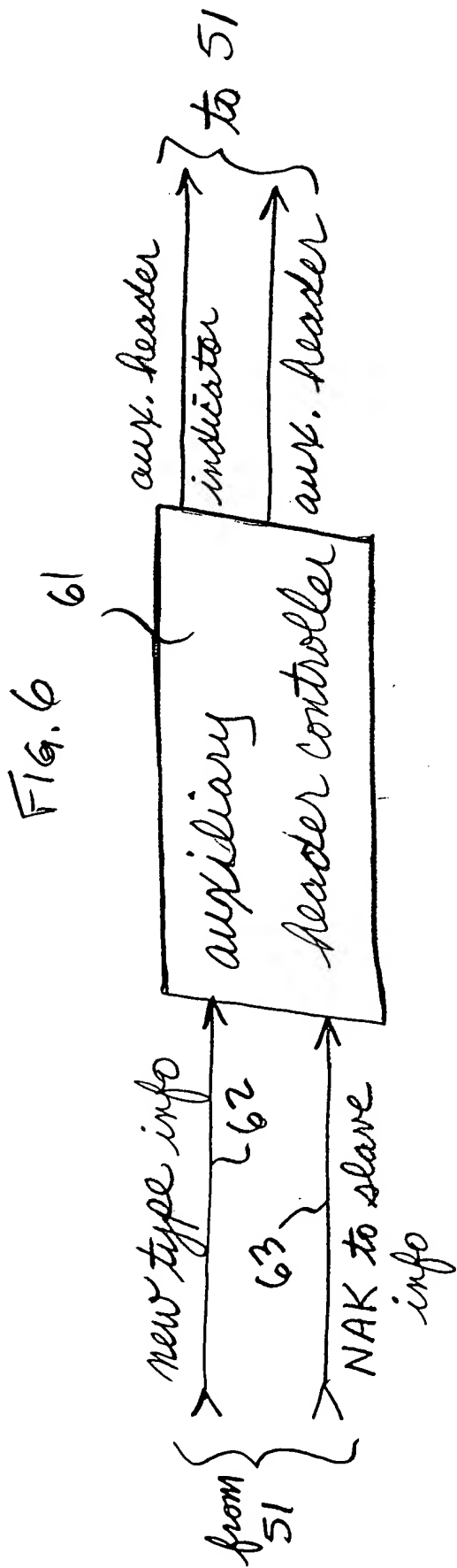
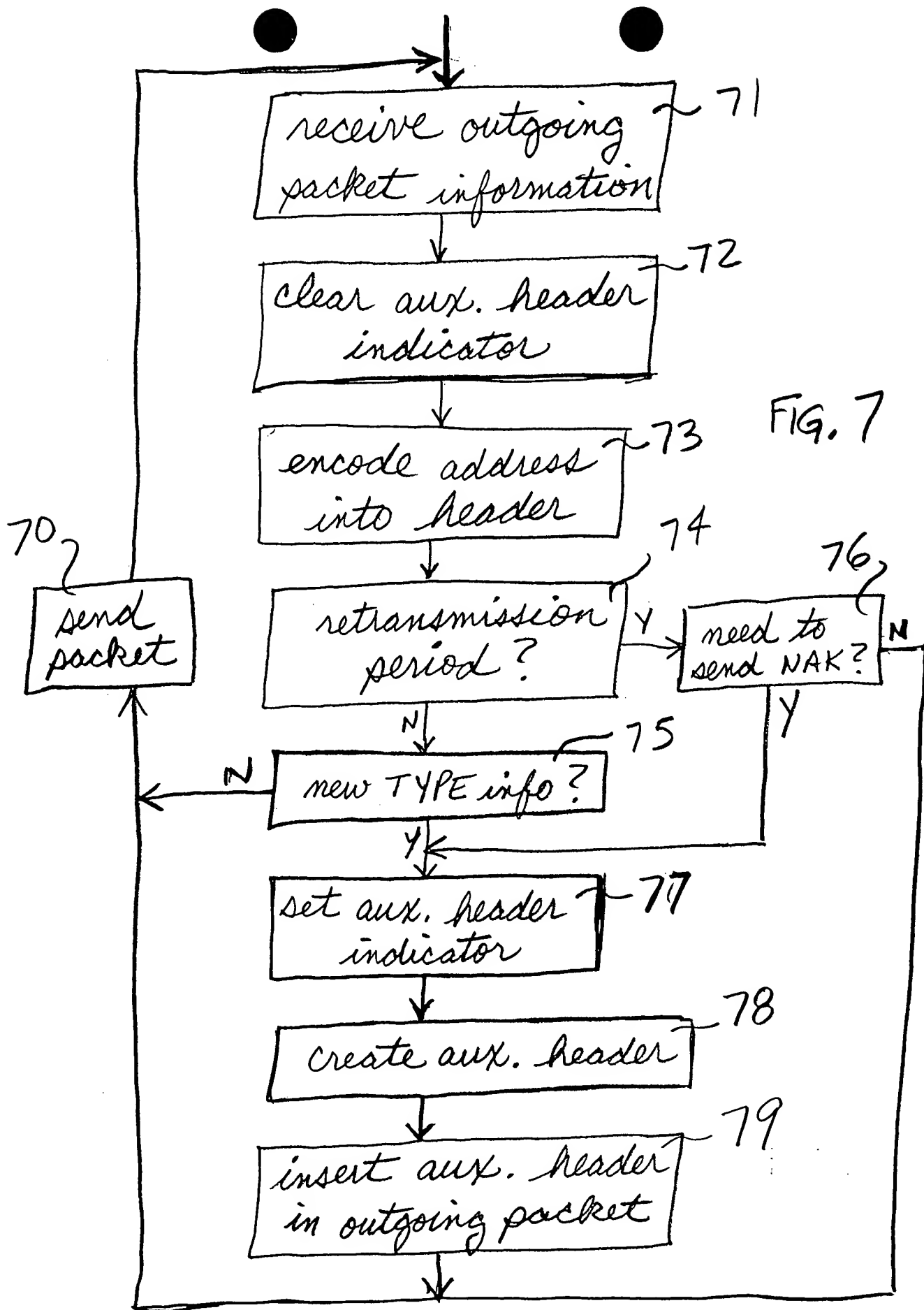
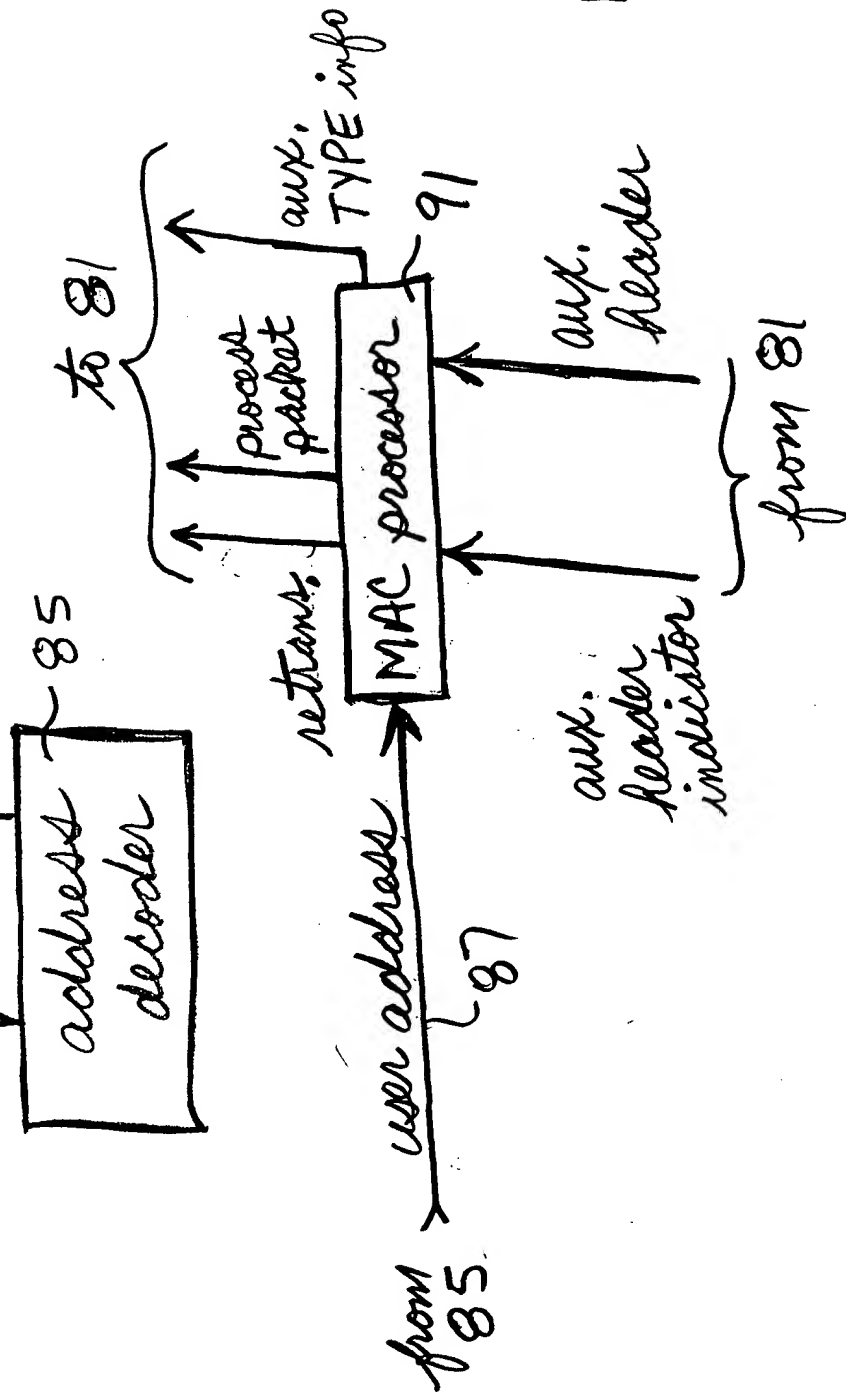
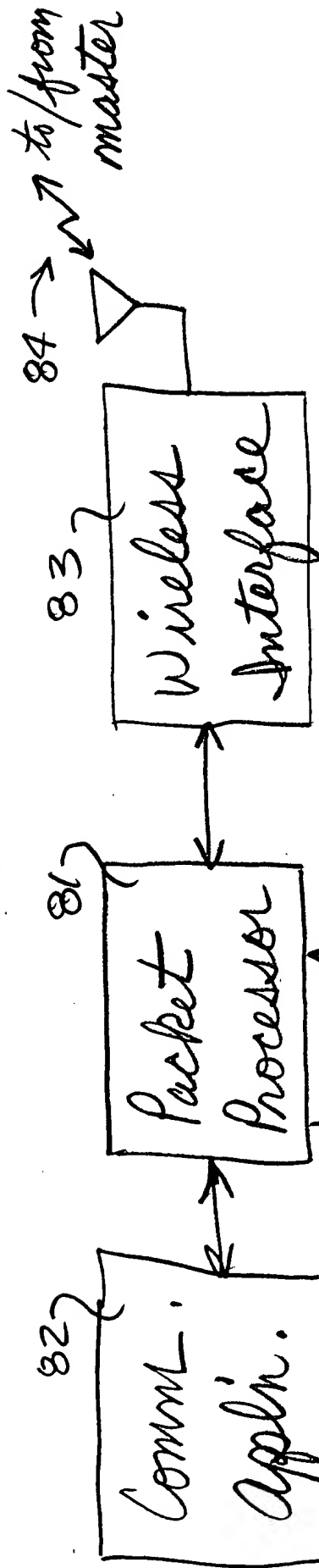


FIG. 5







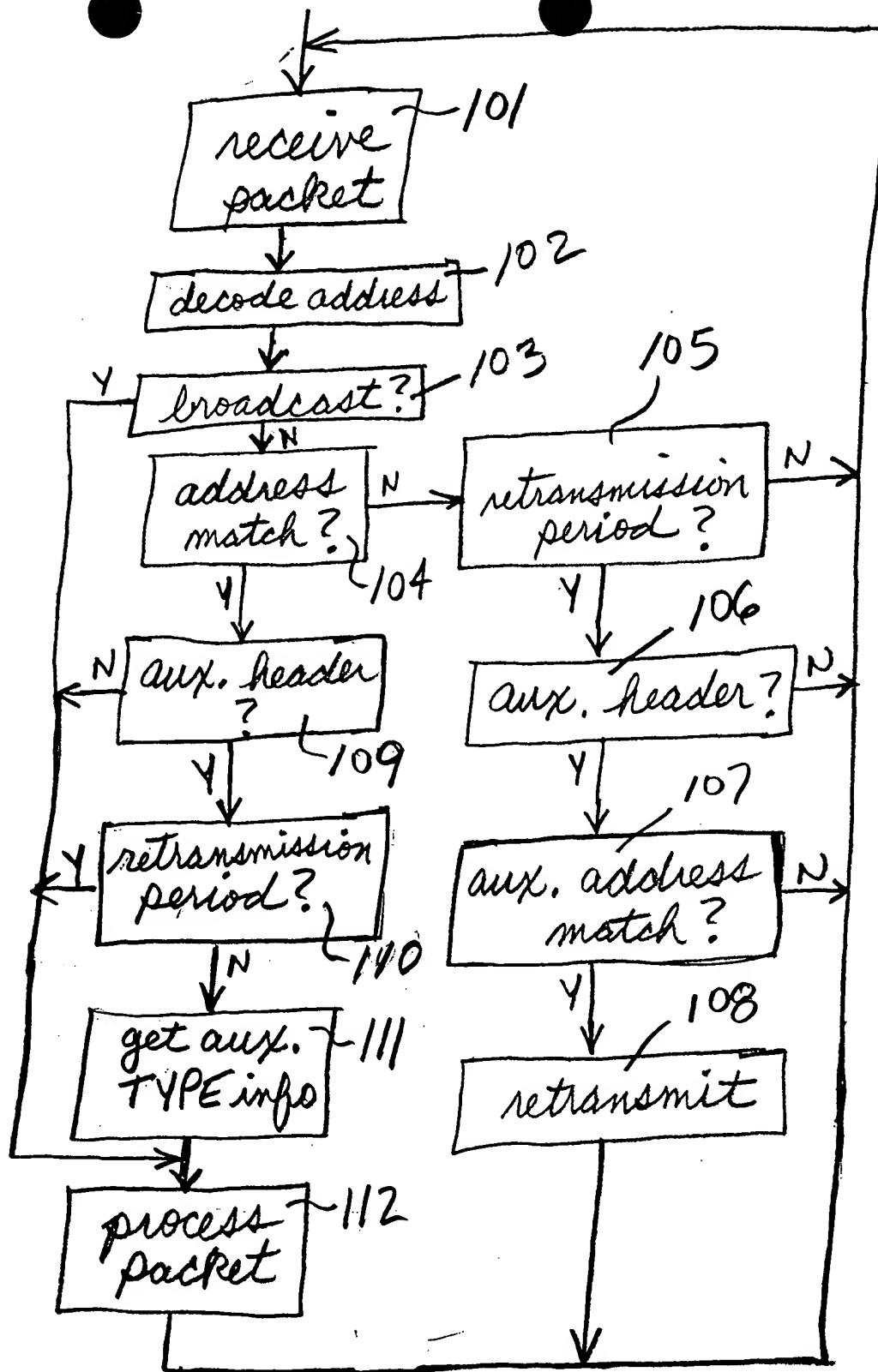


FIG. 10

FIG. 12

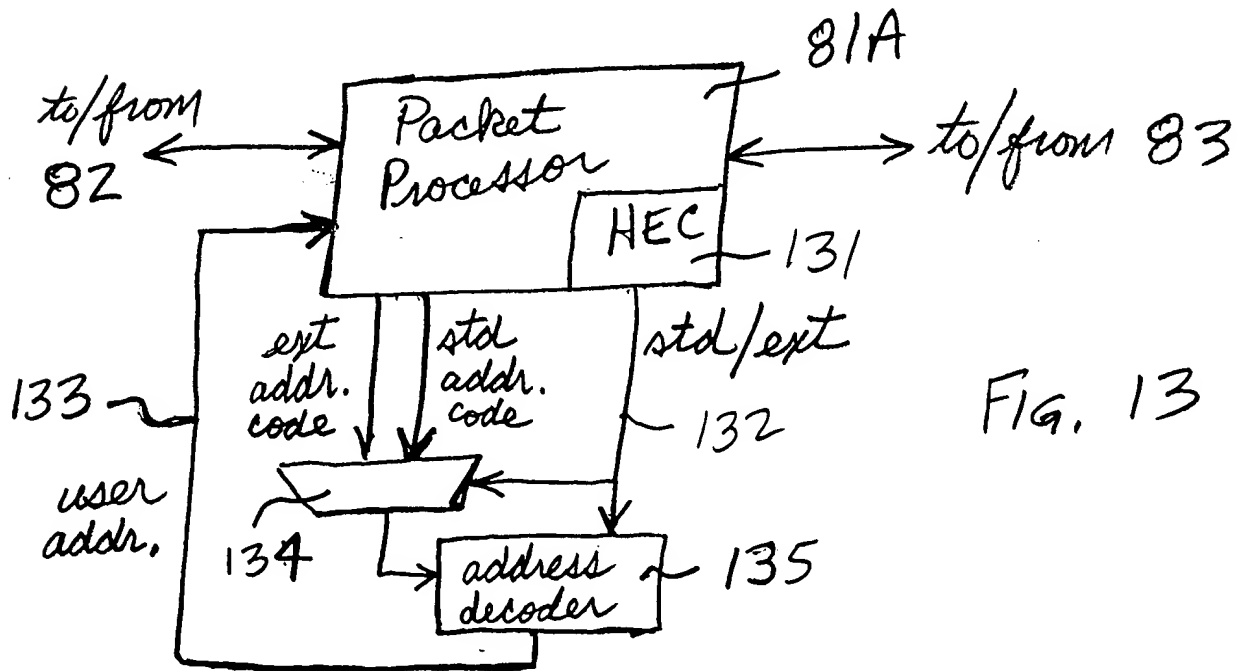
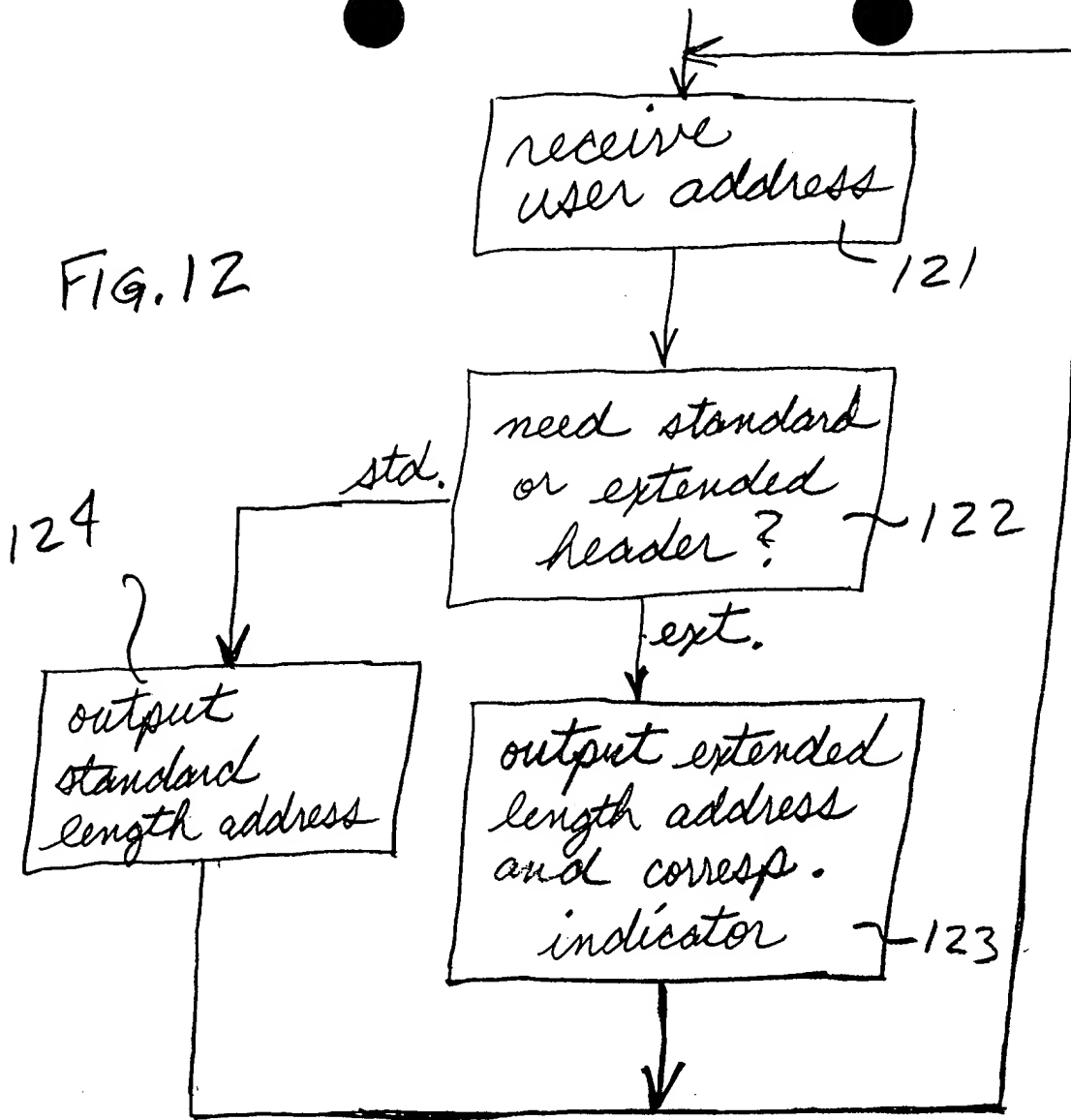


FIG. 13

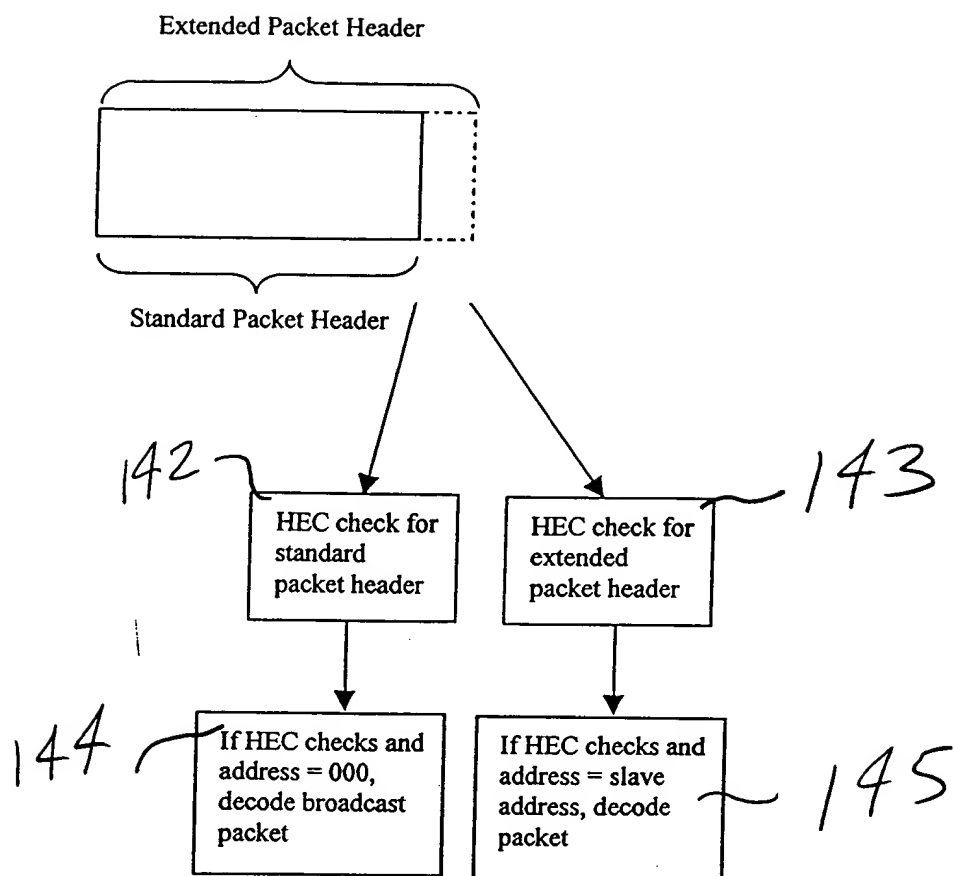


FIG. 14